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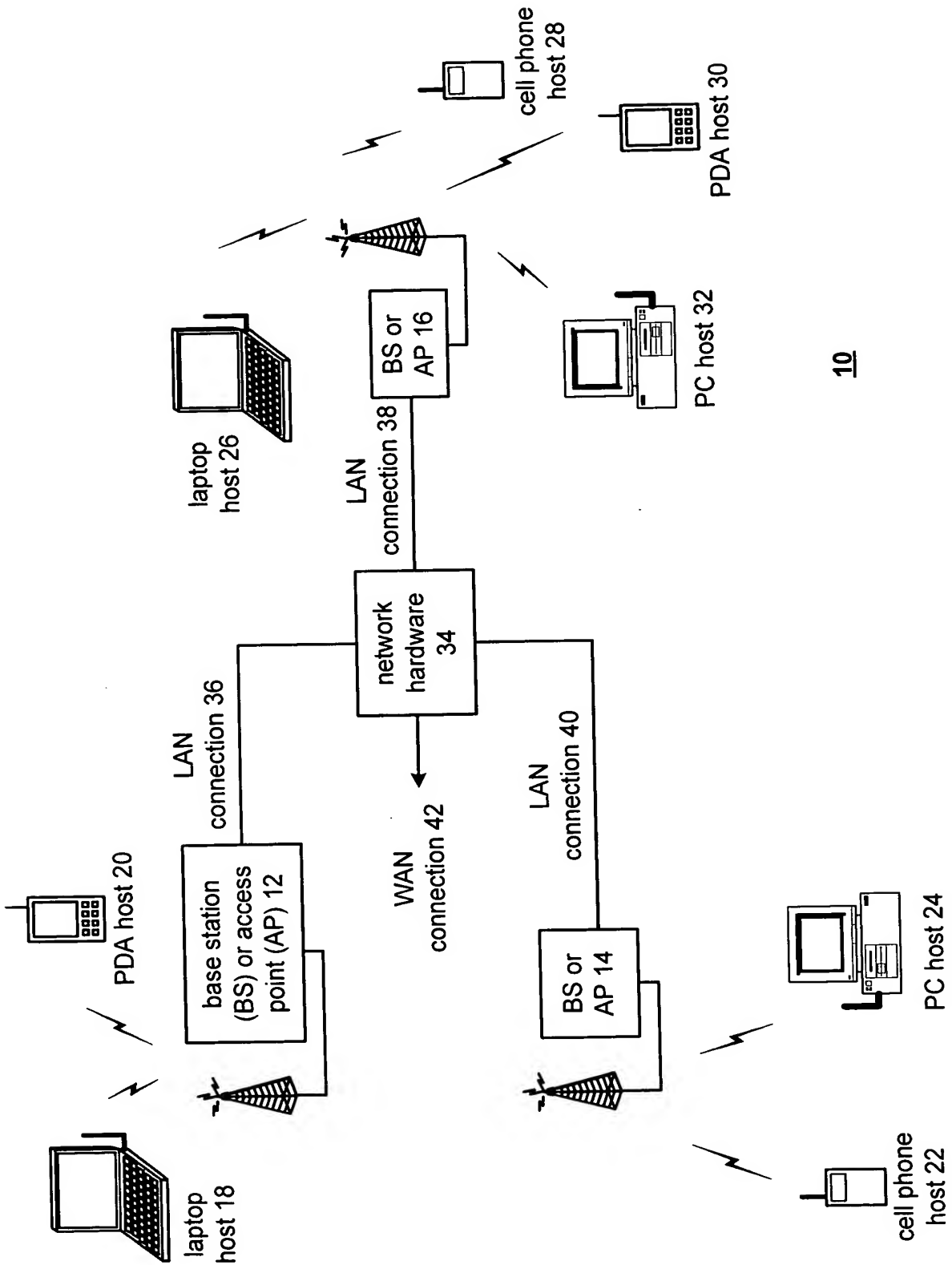
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FIG. 1

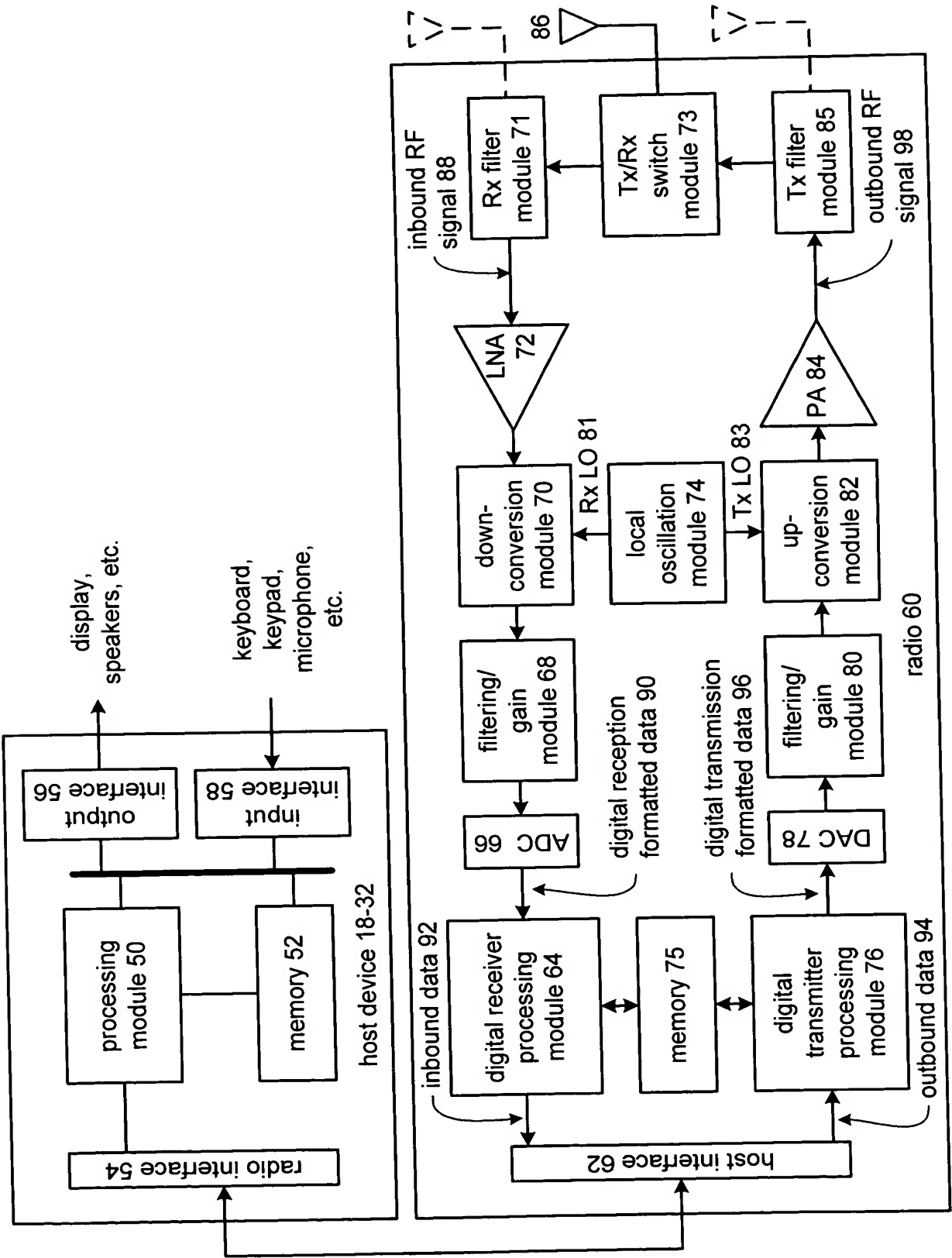


FIG. 2

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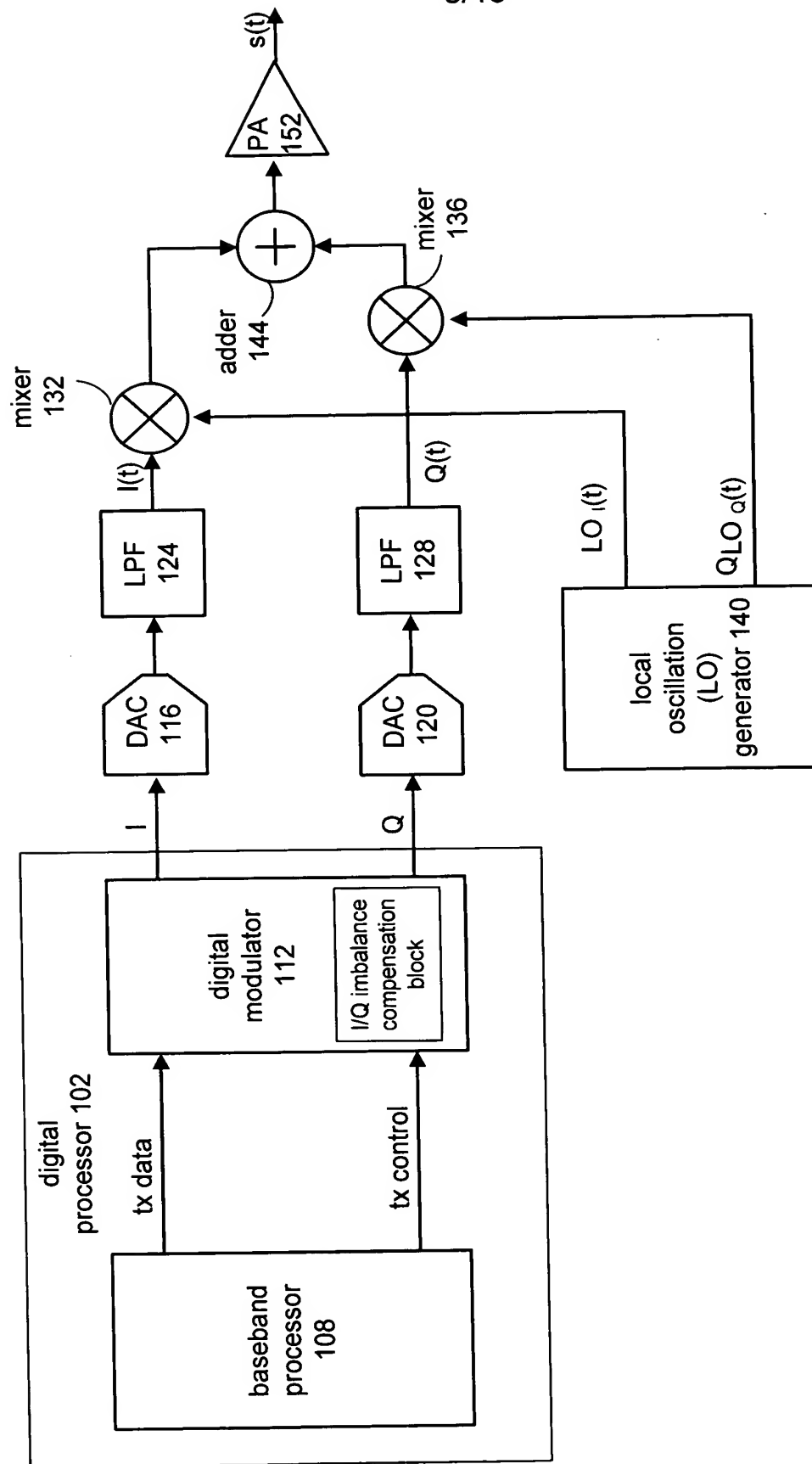
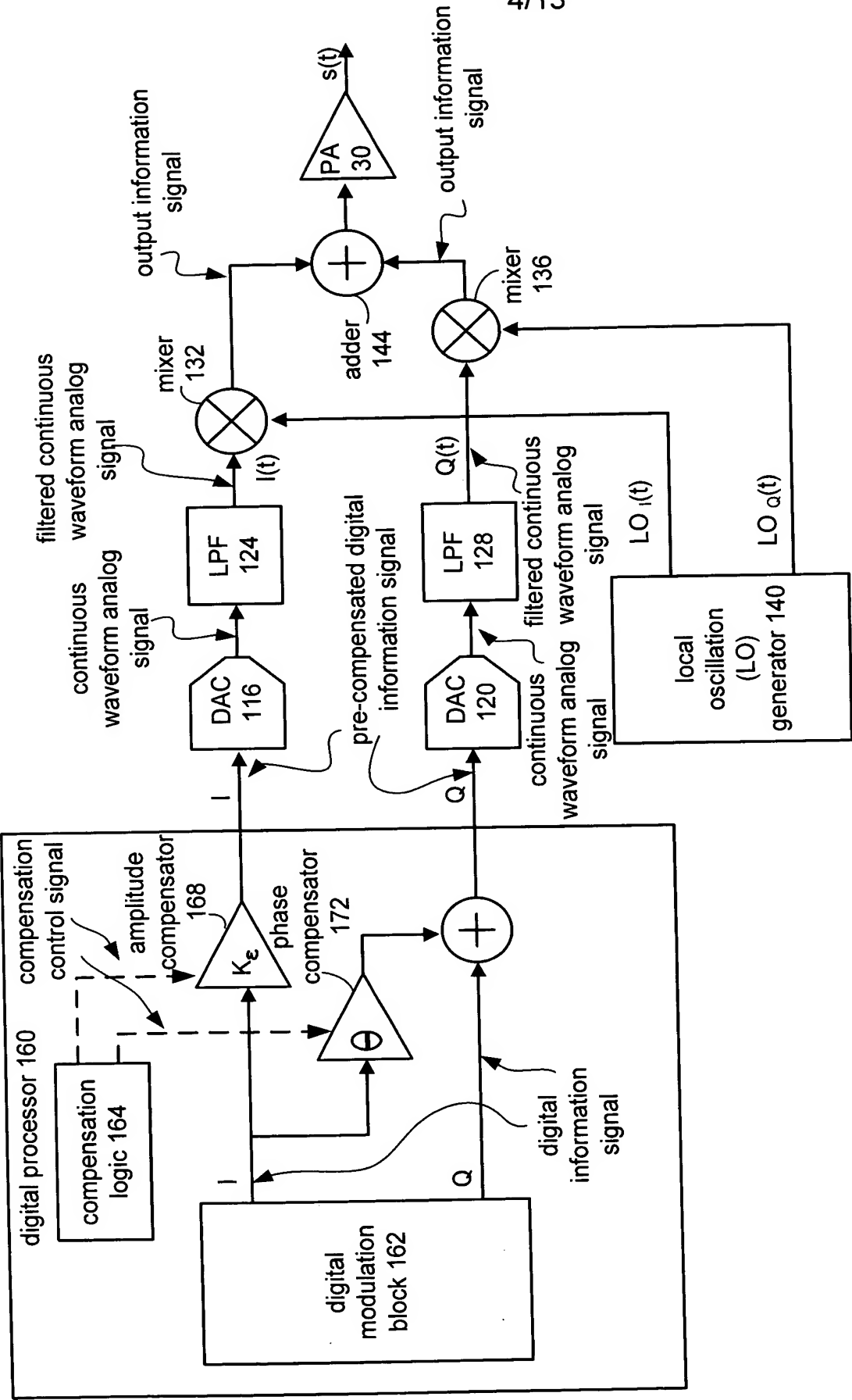
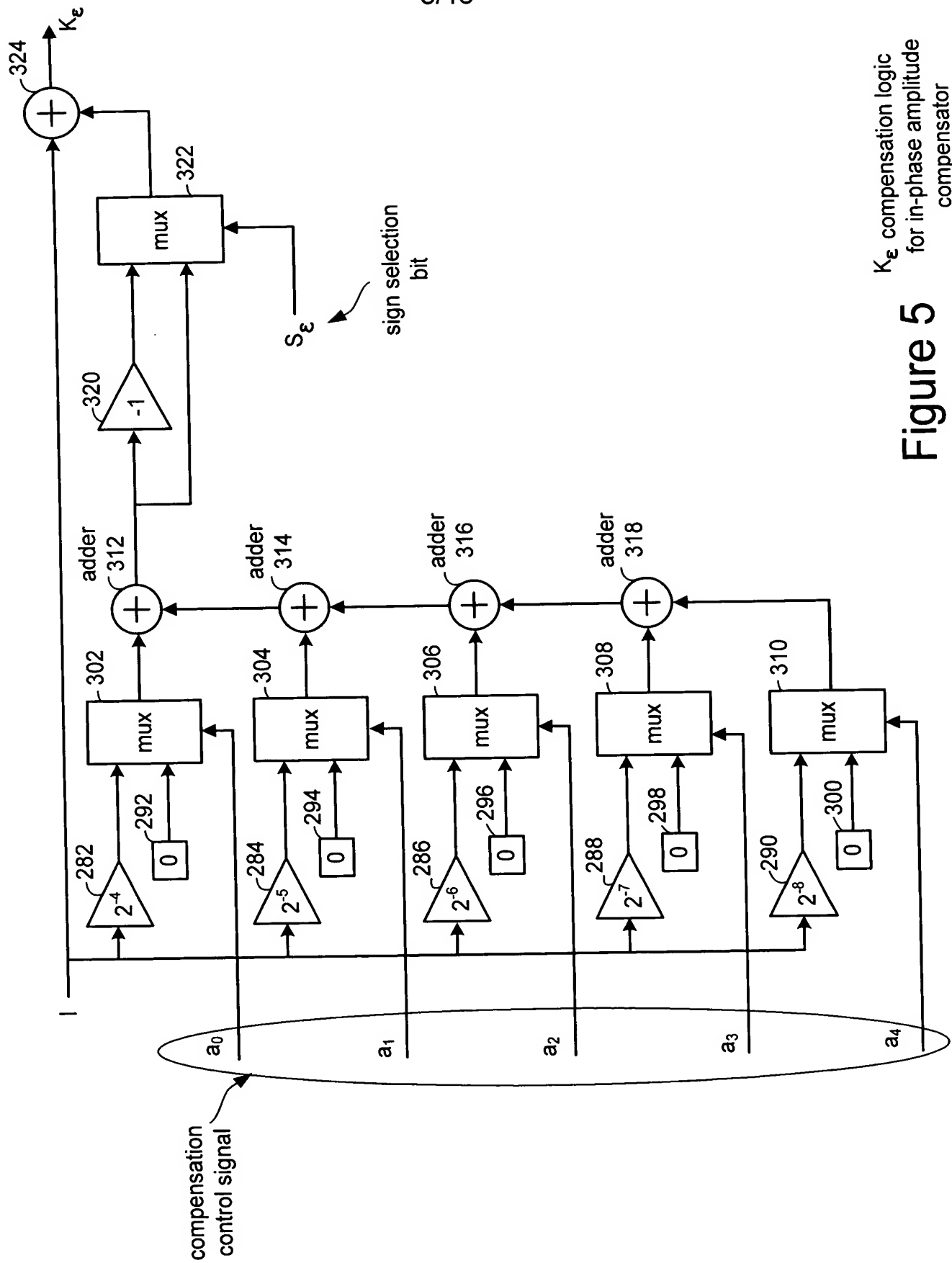


FIG. 3 radio transmitter 100



digital modulator 160 of a  
radio transmitter with I/Q  
imbalance compensation

FIG. 4



$K_e$  compensation logic  
for in-phase amplitude  
compensator

Figure 5

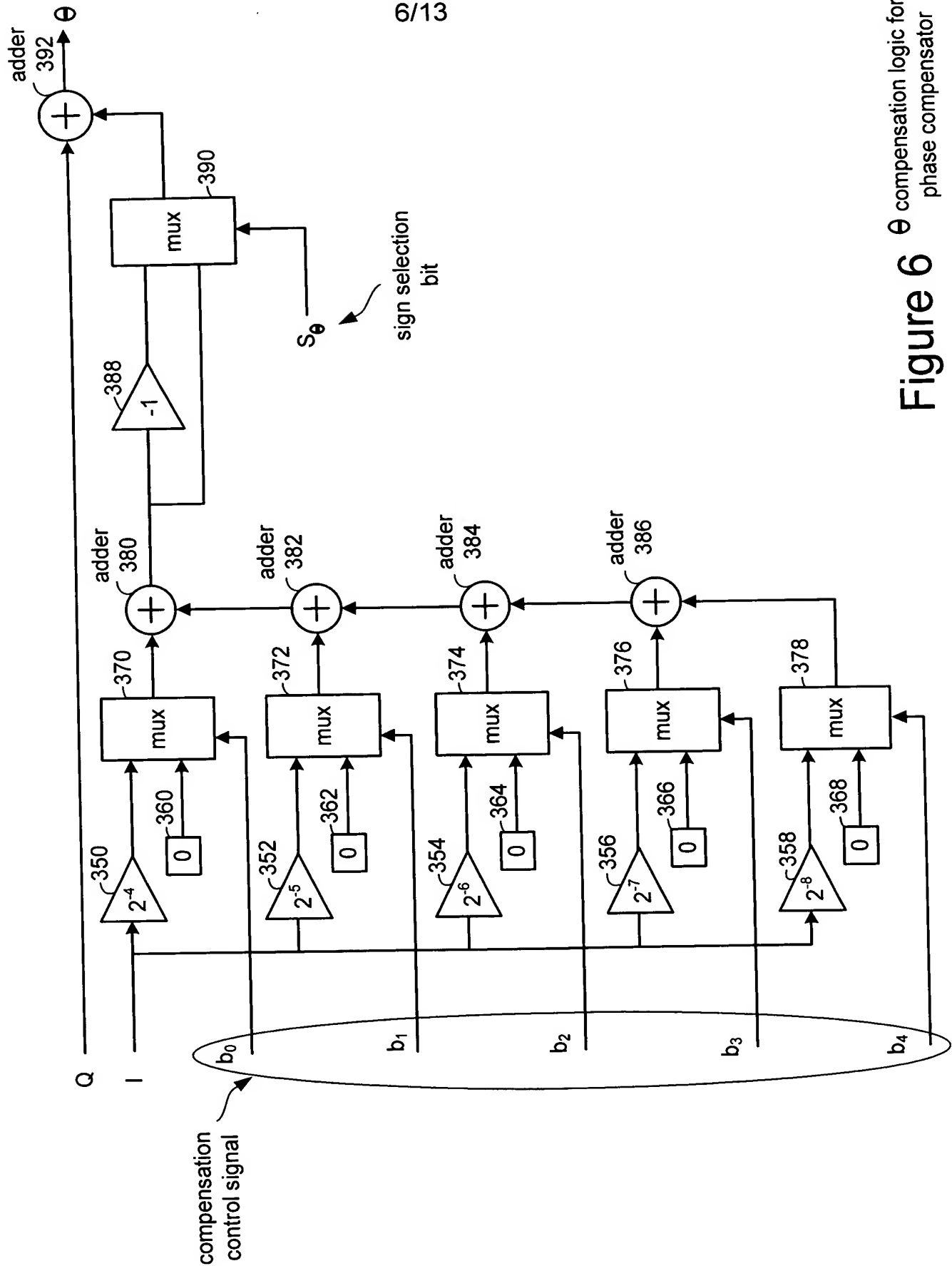


Figure 6  $\theta$  compensation logic for phase compensator

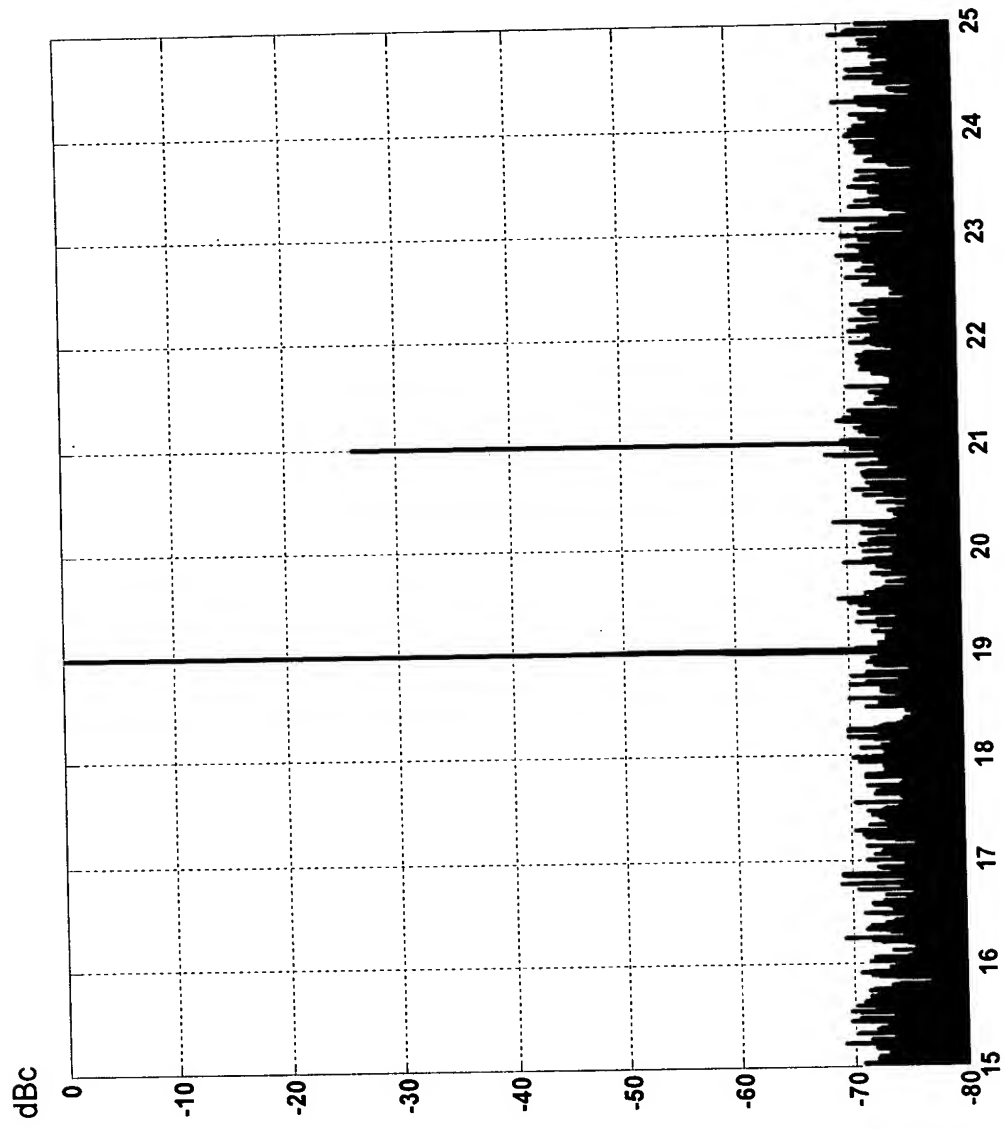


FIG. 7



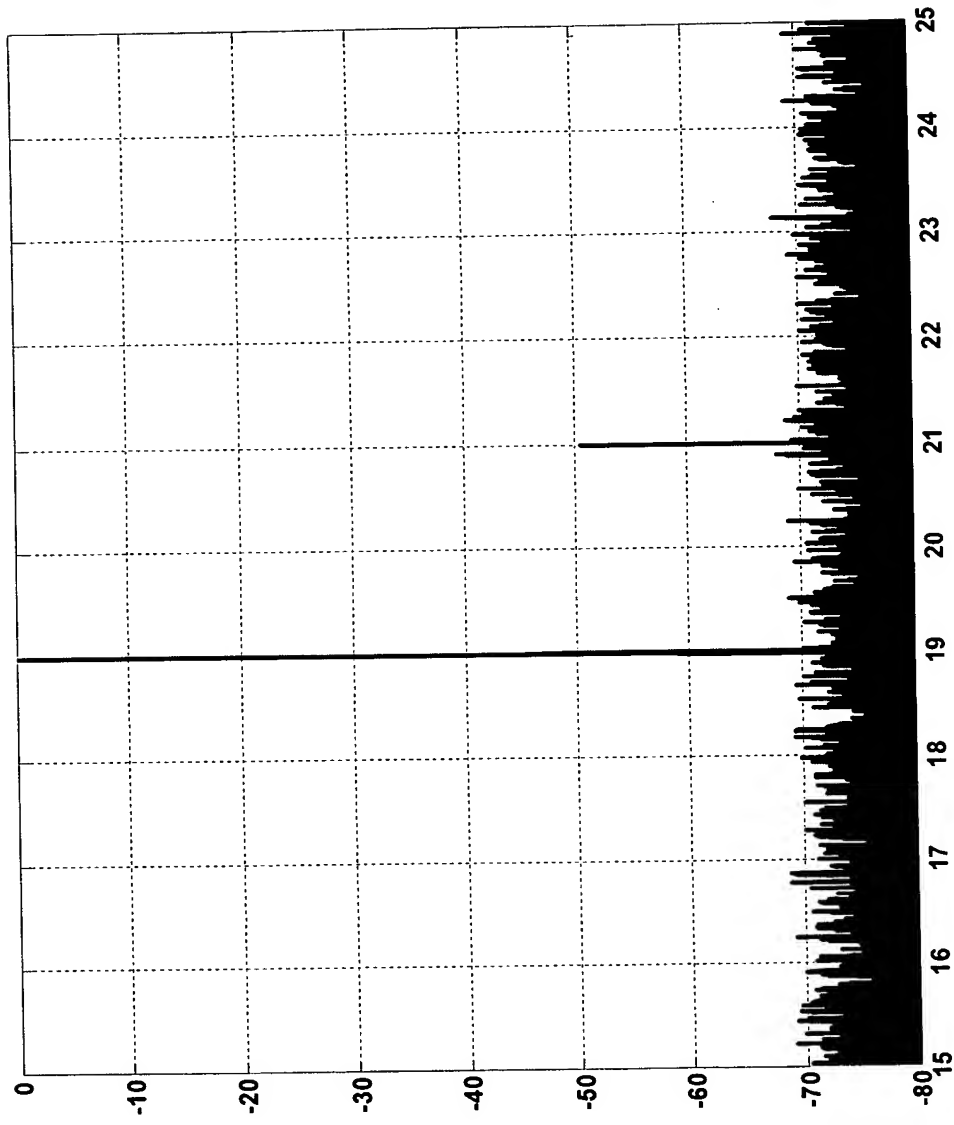


FIG. 8

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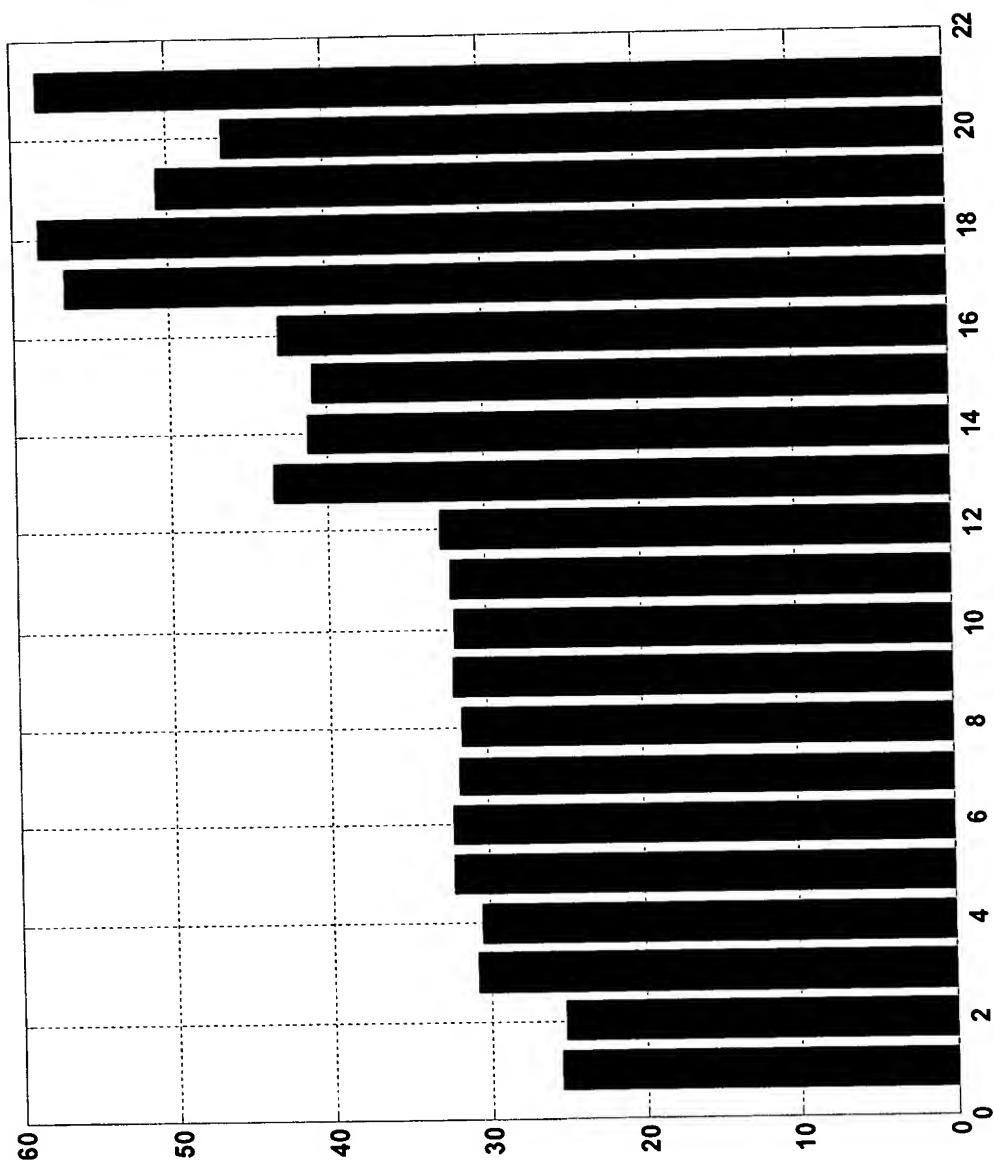


FIG. 9

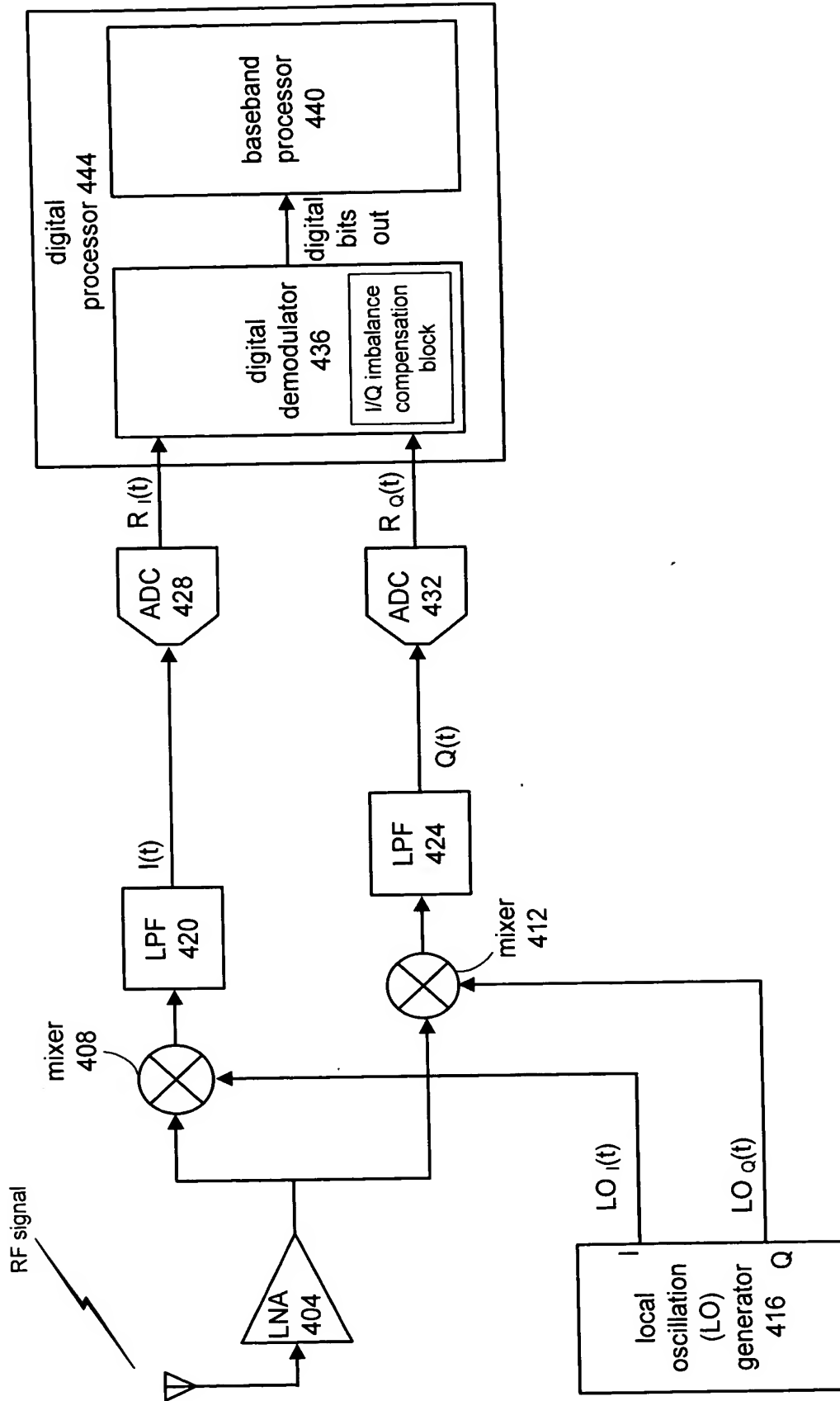


FIG. 10 radio receiver 400

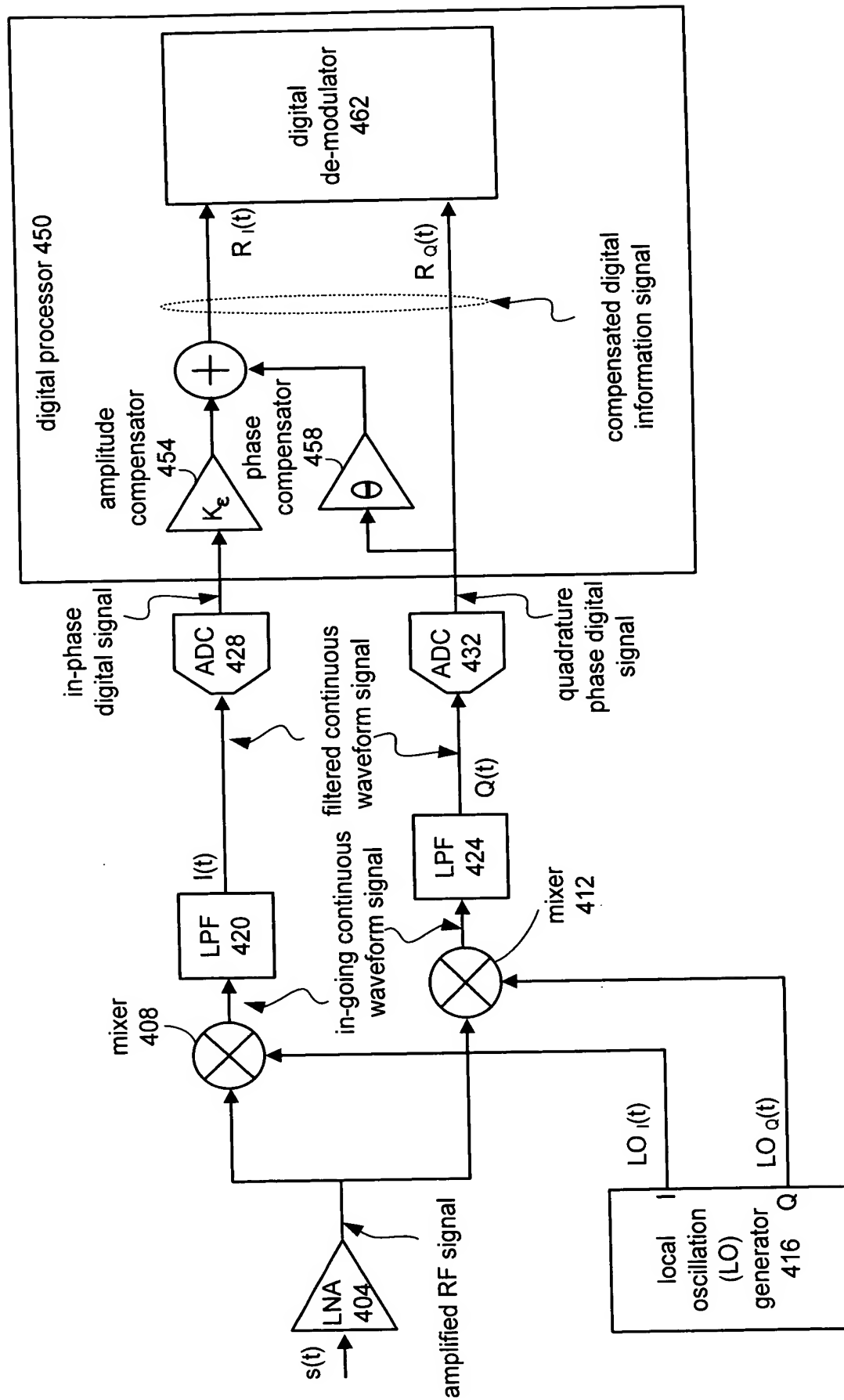
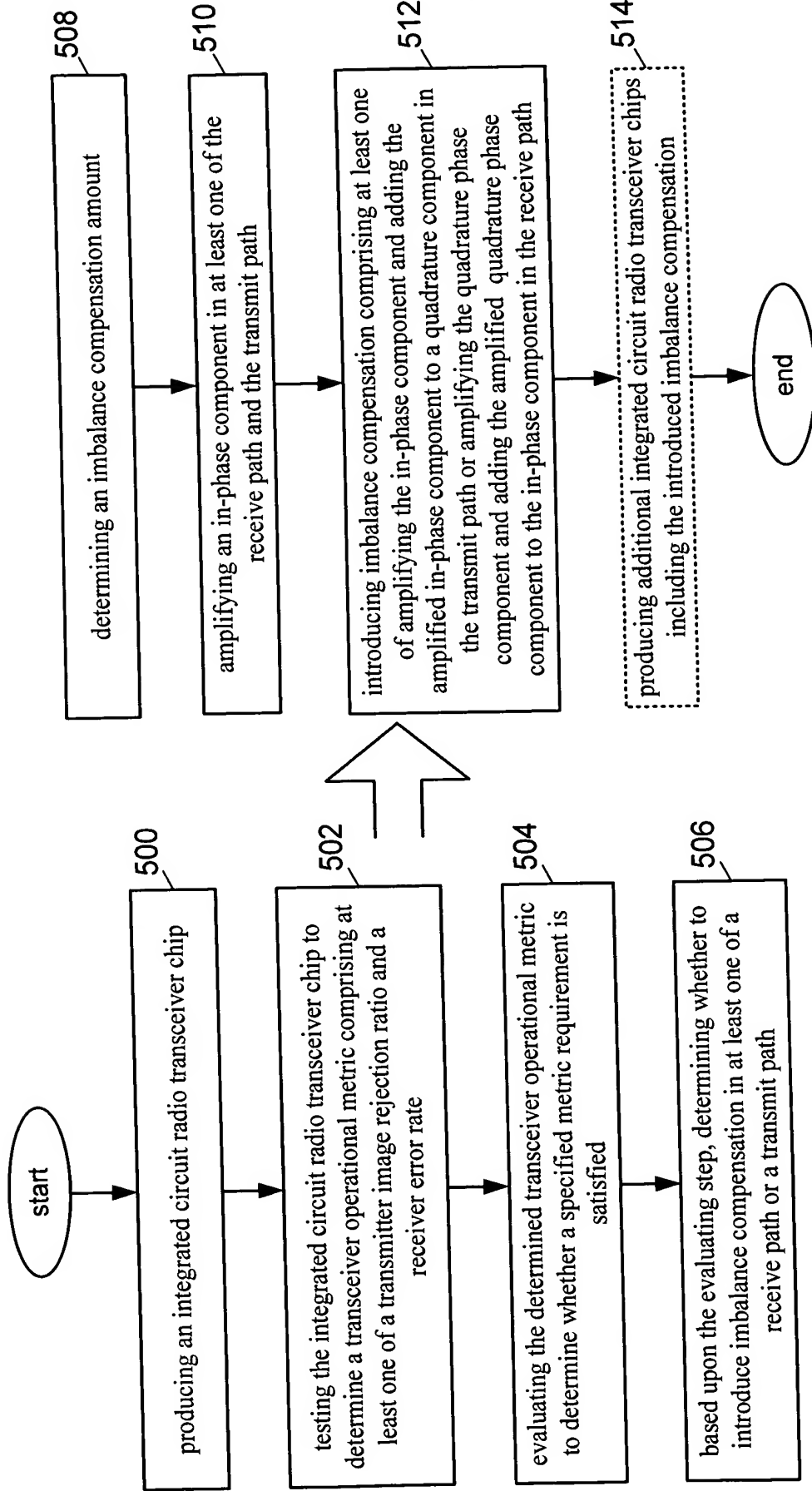
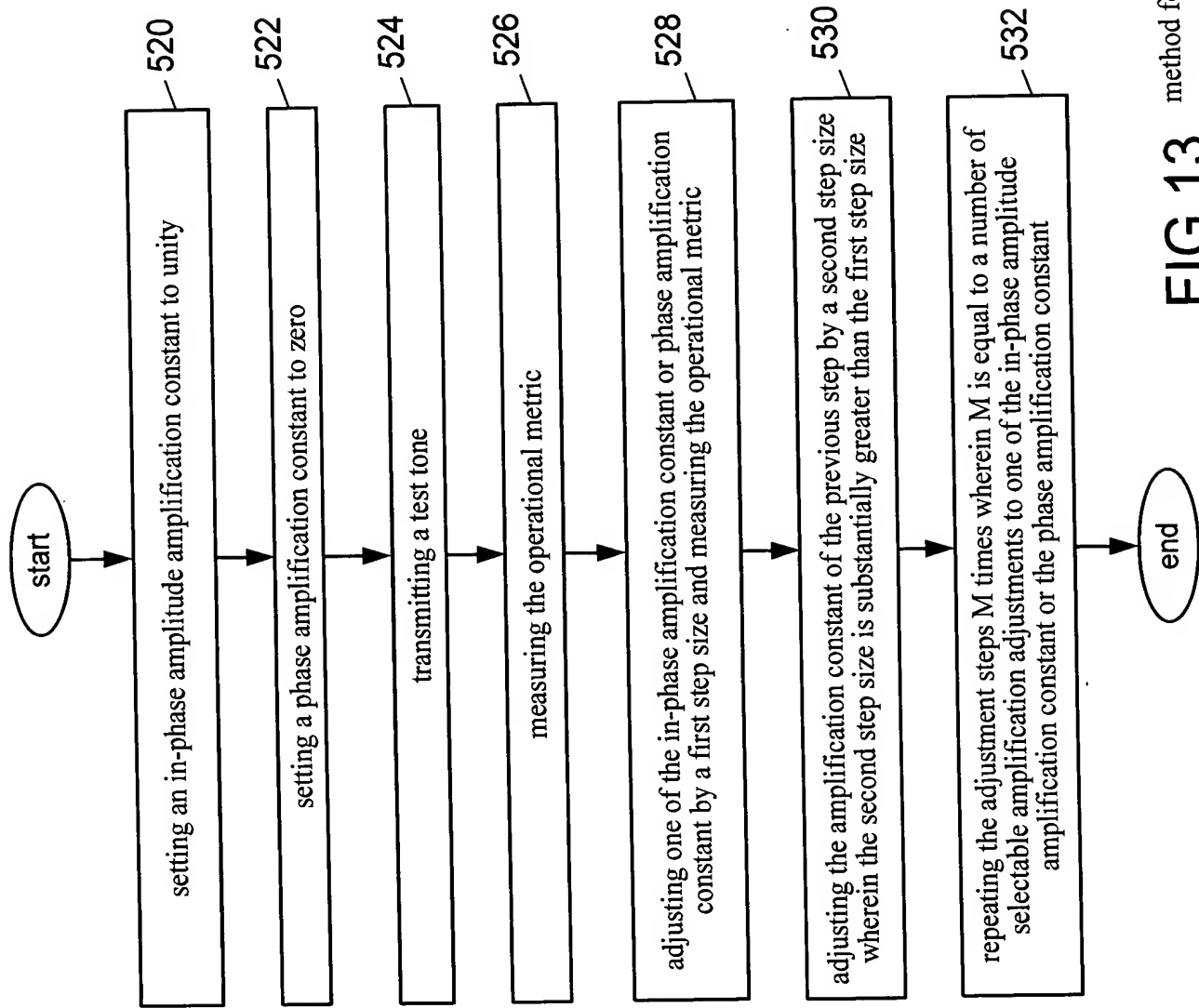


FIG. 11 radio receiver 400



**FIG 12** method for compensating for I/Q imbalance in production chips



**FIG 13** method for selecting imbalance compensation parameters